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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent:

6,973,099  $\beta$ 1

Serial No.: 09/745,655

Issued: 12/06/2005

For:

**SYSTEM AND METHOD FOR PROGRAMMING THE VALUE OF  
FRAME SYNCHRONIZATION WORDS IN A MULTIDIMENSIONAL  
DIGITAL FRAME STRUCTURE**

) Confirmation No. 1814

) Group No.: 2666

) Examiner: R.C. Scheibel, Jr.

) Docket No.: AMCC4840

CERTIFICATION UNDER 37 CFR § 1.8

I hereby certify that the documents referred to as enclosed herein are being deposited with the United States Postal Service as first class mail on this date December 23, 2005 in an envelope addressed to:  
Commissioner for Patents, Box 1450, Alexandria, VA. 22313-1450

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MAIL STOP: REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents

P. O. Box 1450

Alexandria, VA. 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION

The Assignee, APPLIED MICRO CIRCUITS CORPORATION, of the referenced patent hereby requests that a Certificate of Correction be issued to add References Cited by the Examiner in an Office Action dated 06/15/2004.

Respectfully submitted,

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Certificate  
JAN 04 2006  
of Correction

# UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

PATENT NO : 6,973,099 *Bi*

DATED : 12-06-2005

INVENTOR(S) : Bendek, et al

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Add the following to the title page

### References Cited

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